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PATENT NO. : 7,415,031

APPLICATION NO.: 09/918,691

ISSUE DATE : 08/19/2008

INVENTOR(S) : Primrose et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 18, line 5, "an egress diverted packet" should read --an egress undiverted packet--

Column 23, line 52, "structure including a first" should read --structure comprising a first--

Column 24, line 17, "structure including a first" should read --structure comprising a first--

Column 24, line 51, "structure including a first" should read --structure comprising a first--

Column 24, line 57, "insertion logic and" should read --diversion logic and--

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